

IN THE CLAIMS

Please cancel claims 12-20 and 22-30 without prejudice or disclaimer.

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

Claim 1 (cancelled)

Claim 2 (previously presented) A method for facilitating inter-digital signal processing (DSP) data communications comprising the steps of:

reading a first data structure associated with a block of local memory in a first DSP processor core in a complex comprising a plurality of DSP processor cores, wherein said first data structure comprises a first source address indicating a first address of where data is stored in said local memory of said first DSP processor core, wherein said first data structure further comprises an indication of a size of a block of memory, wherein said first data structure further comprises a first destination address indicating a second address of where data is to be stored in a local memory of a second DSP processor core;

initiating a transfer of moving data said size of said block of memory located in said first source address in said local memory of said first DSP processor core to said first destination address in said local memory of said second DSP processor core;

obtaining a pointer to a second data structure from said first data structure;

reading said second data structure, wherein said second data structure comprises a second source address of one of a read pointer and a write pointer, wherein said second data structure further comprises a second destination address of one of said read pointer and said write pointer.

Claim 3 (original) The method as recited in claim 2 further comprising the step of:

initiating a transfer of said write pointer located in said second source address in said local memory of said first DSP processor core to said second destination address in said local memory of said second DSP processor core.

Claim 4 (original) The method as recited in claim 2 further comprising the step of:

initiating a transfer of said read pointer located in said second source address in said local memory of said second DSP processor core to said second destination address in said local memory of said first DSP processor core.

Claim 5 (original) The method as recited in claim 2 further comprising the steps of:

obtaining a pointer to a third data structure from said second data structure;  
reading said third data structure, wherein said third data structure comprises a third source address of one of a read pointer and a write pointer, wherein said third data structure further comprises a third destination address of one of said read pointer and said write pointer.

Claim 6 (original) The method as recited in claim 5 further comprising the steps of:

initiating a transfer of said write pointer located in said second source address in said local memory of said first DSP processor core to said second destination address in said local memory of said second DSP processor core; and

initiating a transfer of said read pointer located in said third source address in said local memory of said second DSP processor core to said third destination address in said local memory of said first DSP processor core.

Claim 7 (original) The method as recited in claim 5 further comprising the steps of:

initiating a transfer of said write pointer located in said third source address in said local memory of said first DSP processor core to said third destination address in said local memory of said second DSP processor core; and

initiating a transfer of said read pointer located in said second source address in said local memory of said second DSP processor core to said second destination address in said local memory of said first DSP processor core.

Claim 8 (original) The method as recited in claim 2 further comprising the steps of:

converting a local address of said write pointer to a global address; and  
computing said first source address in said first data structure, wherein said first source address is equal to said size of a block of memory subtracted from said global address of said write pointer.

Claim 9 (original) The method as recited in claim 8 further comprising the steps of:  
reading said local address of said write pointer; and  
copying said local address of said write pointer into an entry in a third data structure located in said first DSP processor core.

Claim 10 (original) The method as recited in claim 8 further comprising the steps of:  
reading a local address of said read pointer; and  
copying said local address of said read pointer into an entry in a third data structure located in said second DSP processor core.

Claims 11-30 (cancelled)